

FIG. 1 is a block diagram of a system 100.

Figure 1

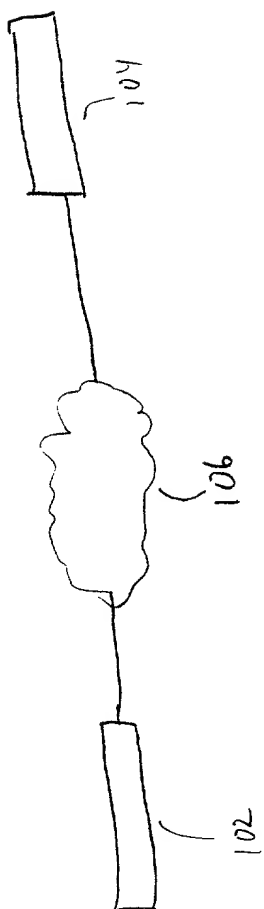


Figure 2

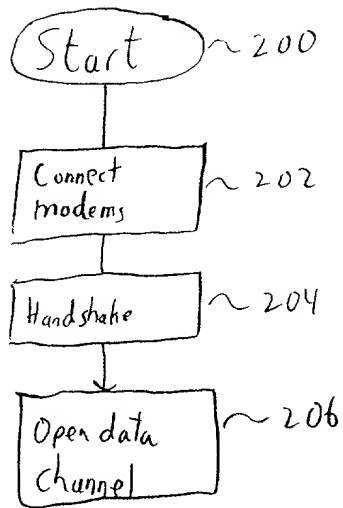


FIG. 2 is a flowchart of a process for establishing a data link between a first computer and a second computer. The process begins at a start point, proceeds to a step of connecting the modems, then to a handshake step, and finally to an open data channel step, before ending.

Figure 3

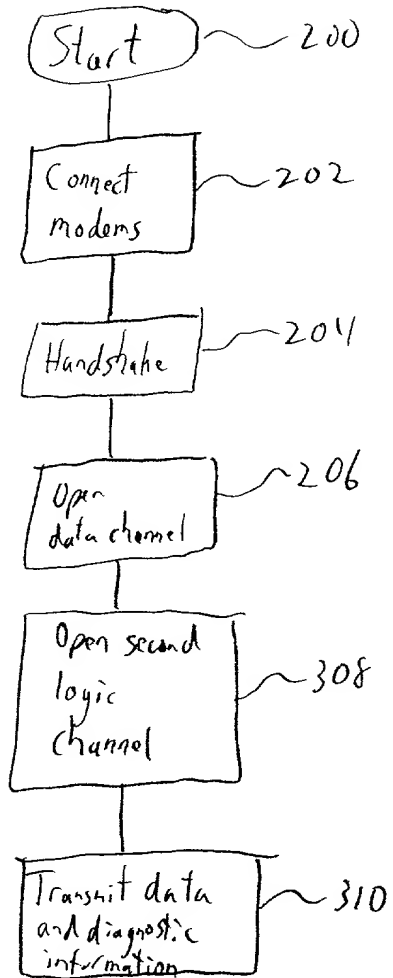


FIG. 4 is a block diagram of a network architecture.

Figure 4

400

4067

4047

4027

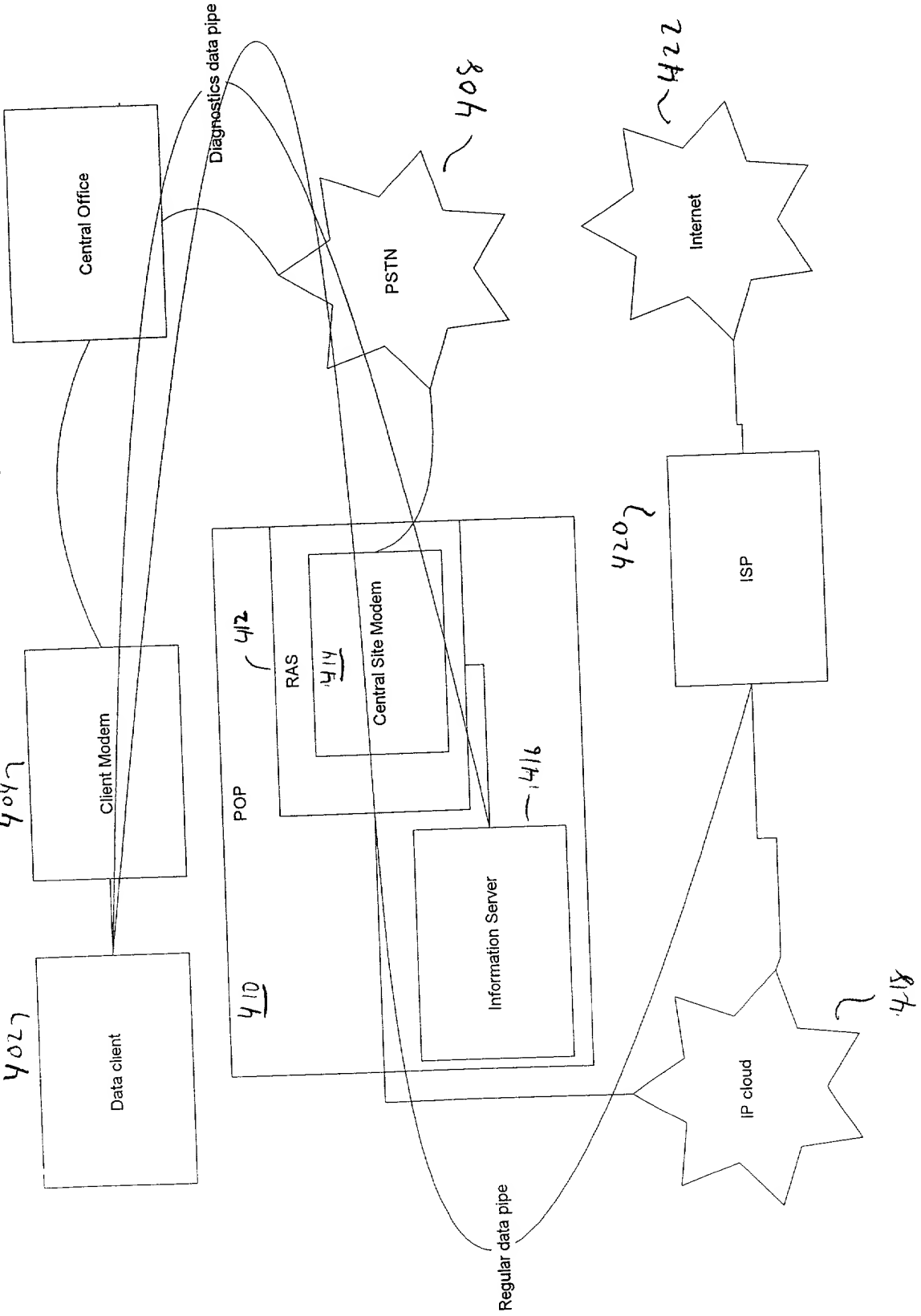


Figure 5

502 }	504 }	506 }	508 }	510 }	512 }	514 }
OPEN_FLAG	DLCI	UI_CONTROL	UI_TYPE	UI_INFO	CRC	CLOSE_FLAG
Flag	Standard Address Field	Standard UI Control Field	First byte of Information field	Remaining Information Field (Secondary Channel Frame)	Checksum (CRC-16)	Flag
1 byte	1 byte	1 byte	1 byte	Up to 248 bytes	2 bytes	1 byte

Figure 6

FINAL_FRAME	SEQ_NUM	FRAME_TYPE	DIAG_CODE	DIAG_INFO
Final Frame	Sequence Number	First byte of Information field (frame type)	Second and third bytes of Information field (Diagnostic code)	Remaining Information Field (remaining data payload)
1 bit	7 bits	1 byte	2 bytes	Up to 244 bytes

602

604

606

608

610